

<b>Notice of References Cited</b>	Application/Control No. 10/750,150	Applicant(s)/Patent Under Reexamination HAMMOND ET AL.	
	Examiner Benjamin P. Geib	Art Unit 2181	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Gharachorloo et al.; "Two Techniques to Enhance the Performance of Memory Consistency Models"; 1991; Proceedings of the 1991 International Conference on Parallel Processing
	V	Huck et al.; "Introducing the IA-64 Architecture"; 2000; IEEE
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	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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